CLIPPEDIMAGE= JP404292039A
PAT-NO: JP404292039A
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TITLE: COMMUNICATION PROCESSOR
PUBN-DATE: October 16, 1992
INVENTOR-INFORMATION:
NAME
HORI, KENICHI
ASSIGNEE-INFORMATION:
NAME   COUNTRY
NEC CORP   N/A
APPL-NO: JP03081755
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INT-CL_(IPC): H04L012/56; G06F013/00 ; H04L029/10
ABSTRACT:

PURPOSE: To reduce overhead on a channel, and to improve performance by dividing a packet transmitted from a host computer into frame length shorter than the maximum frame length determined by the properties of various sub-networks .

CONSTITUTION: When a packet assembling means 2C installed in a network layer protocol executing means 2A repeats the packet received from the sub- networks 4,5 a communication processor 2 supports actually like a LAN or a line to the host computer 1, it delivers the packet to the data link control means ID of a host computer 1 side after re-assembling the packet. The assembled packet is the same thing as the packet generated at the time when another host computer which is the opposite party of communication from the beginning intends to send the packet first to the network . Thus, the size of the packet to be delivered to the host computer can be made larger independently of the maximum frame length of the sub-network .